

Fig. 1A

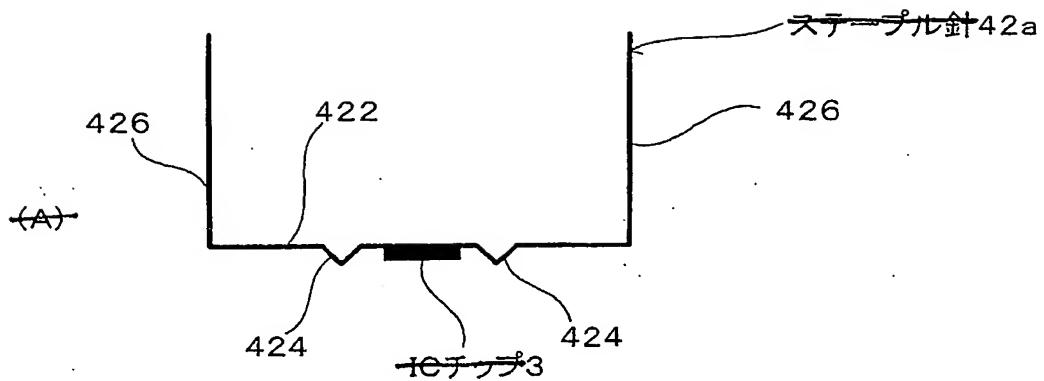


Fig. 1B

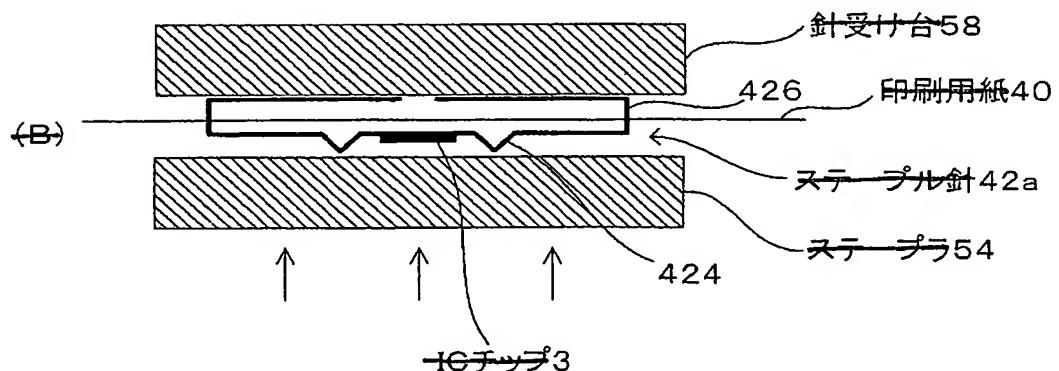


Fig. 2

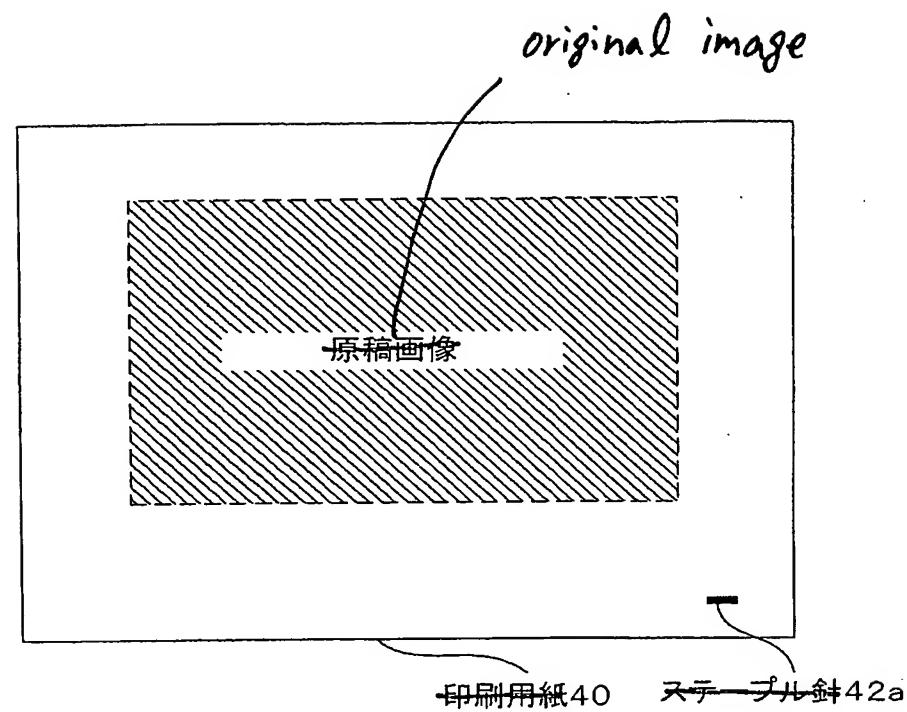


Fig. 3

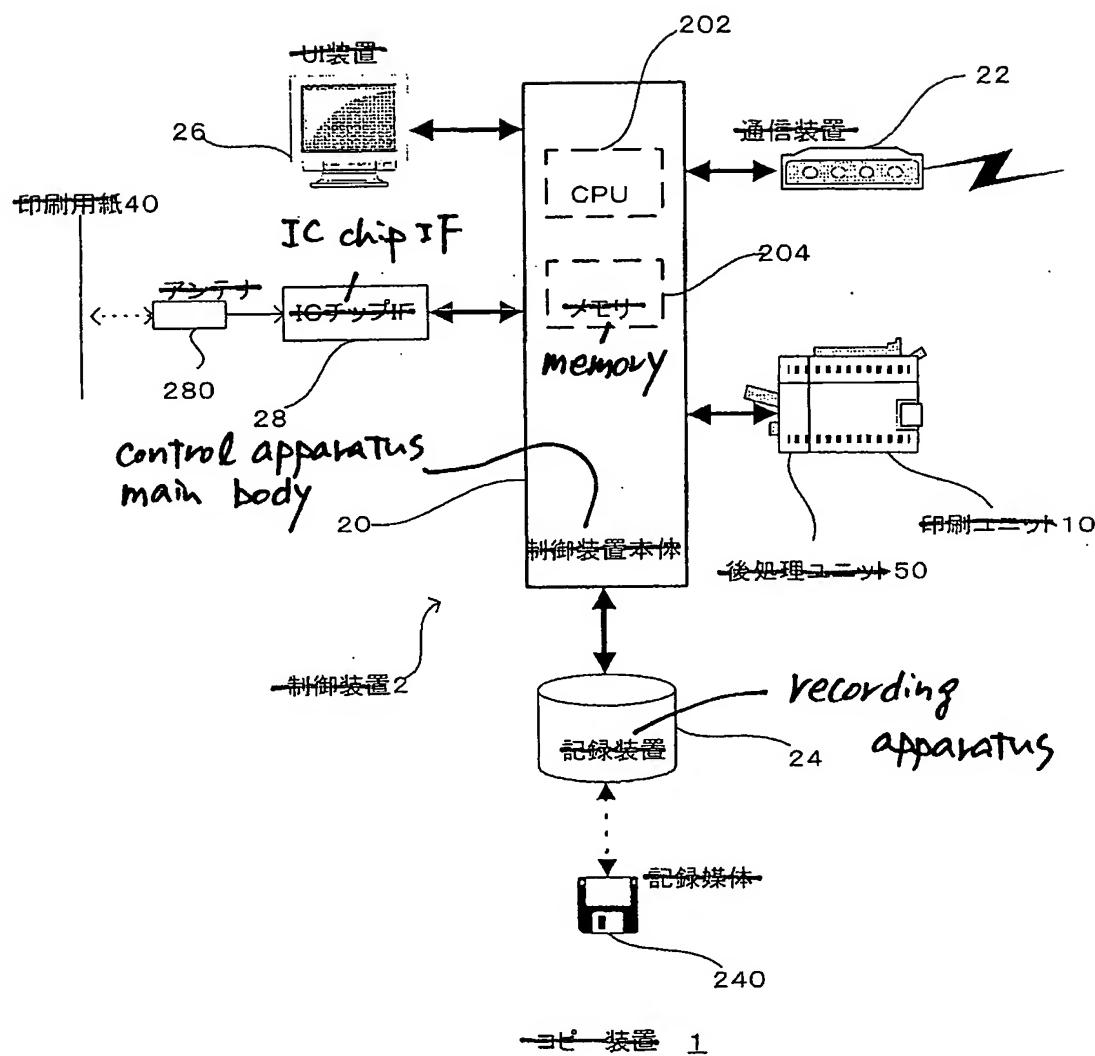


Fig. 4

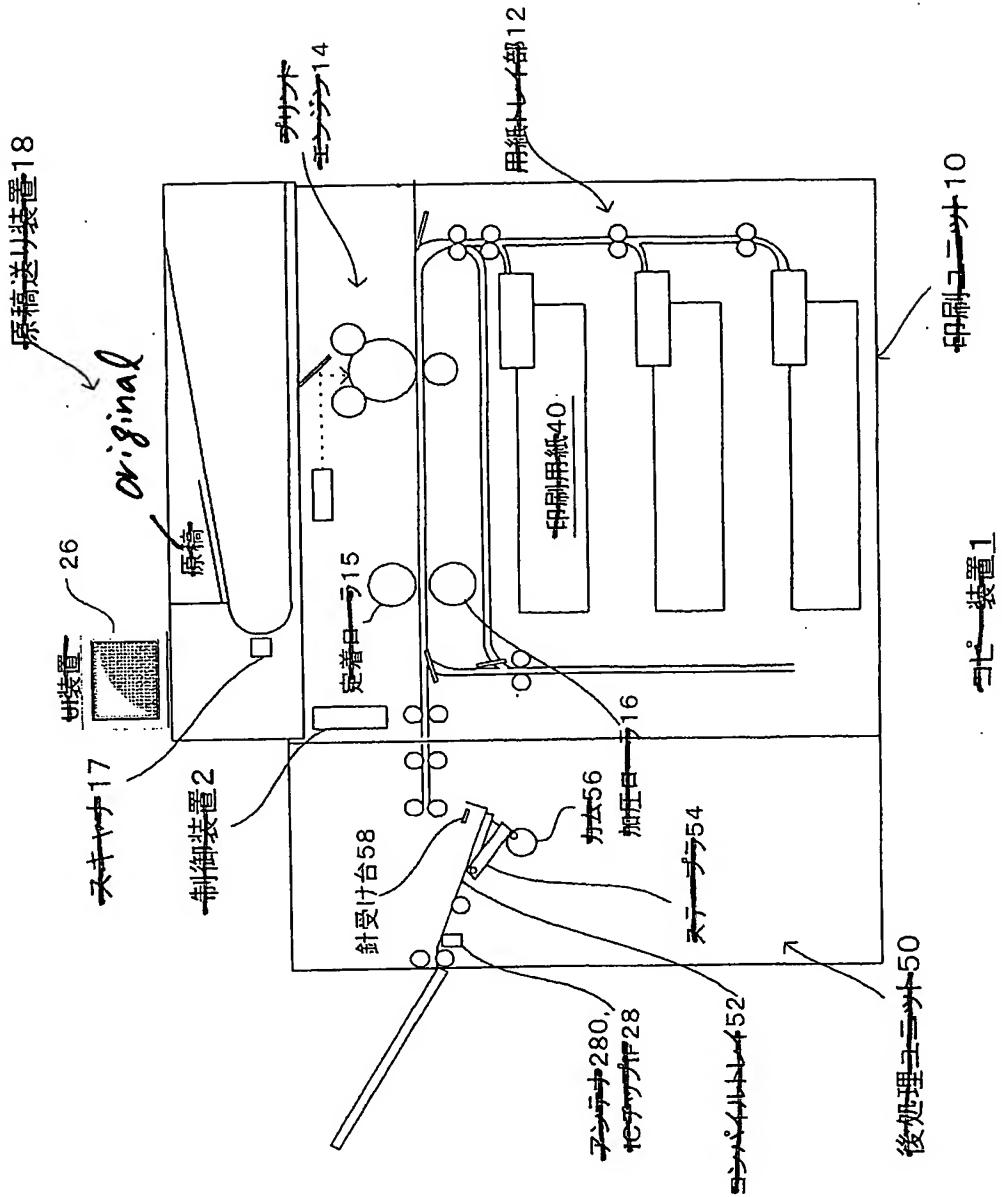


Fig. 5

staple needle (portion serving as an antenna)

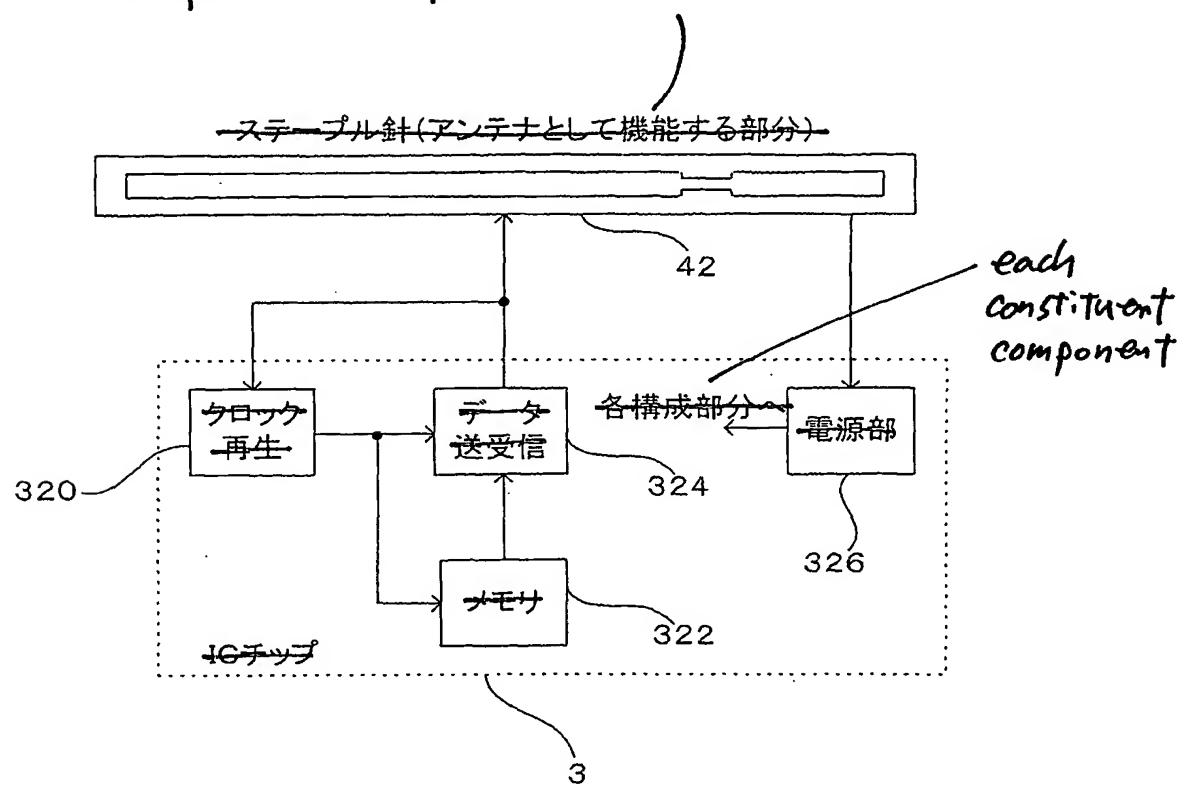


FIG. 5

320 clock reproducing circuit
322 memory circuit
324 data transmitting/receiving circuit
326 power supply circuit

Fig. 6

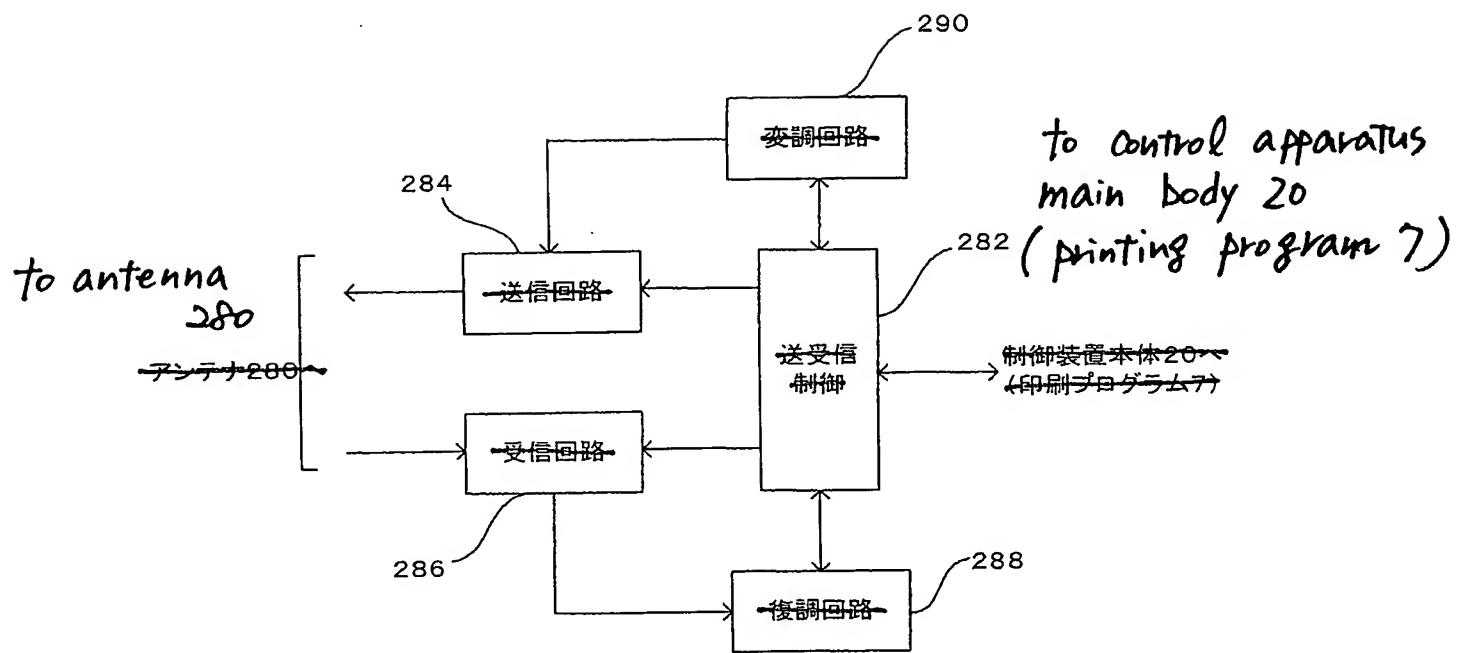


FIG. 6

284 transmitting circuit
286 receiving circuit
290 modulating circuit
282 transmission/reception control circuit
288 demodulating circuit

Fig. 7

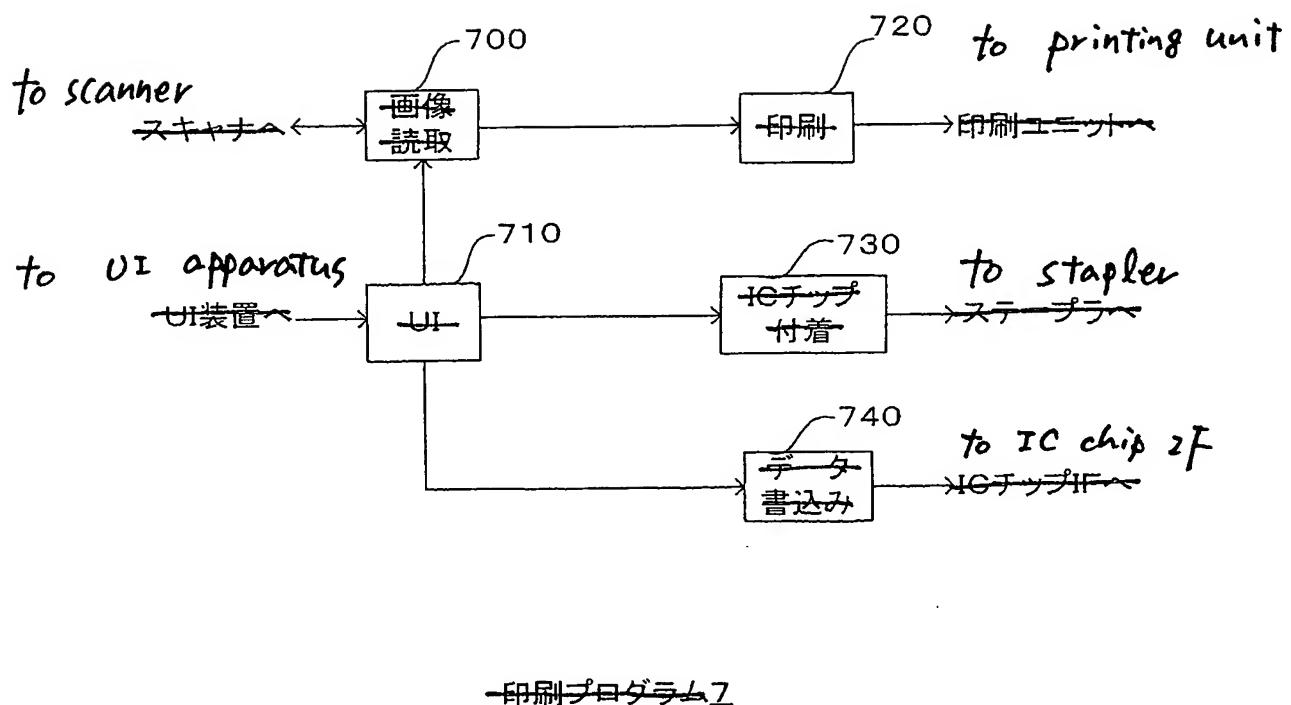
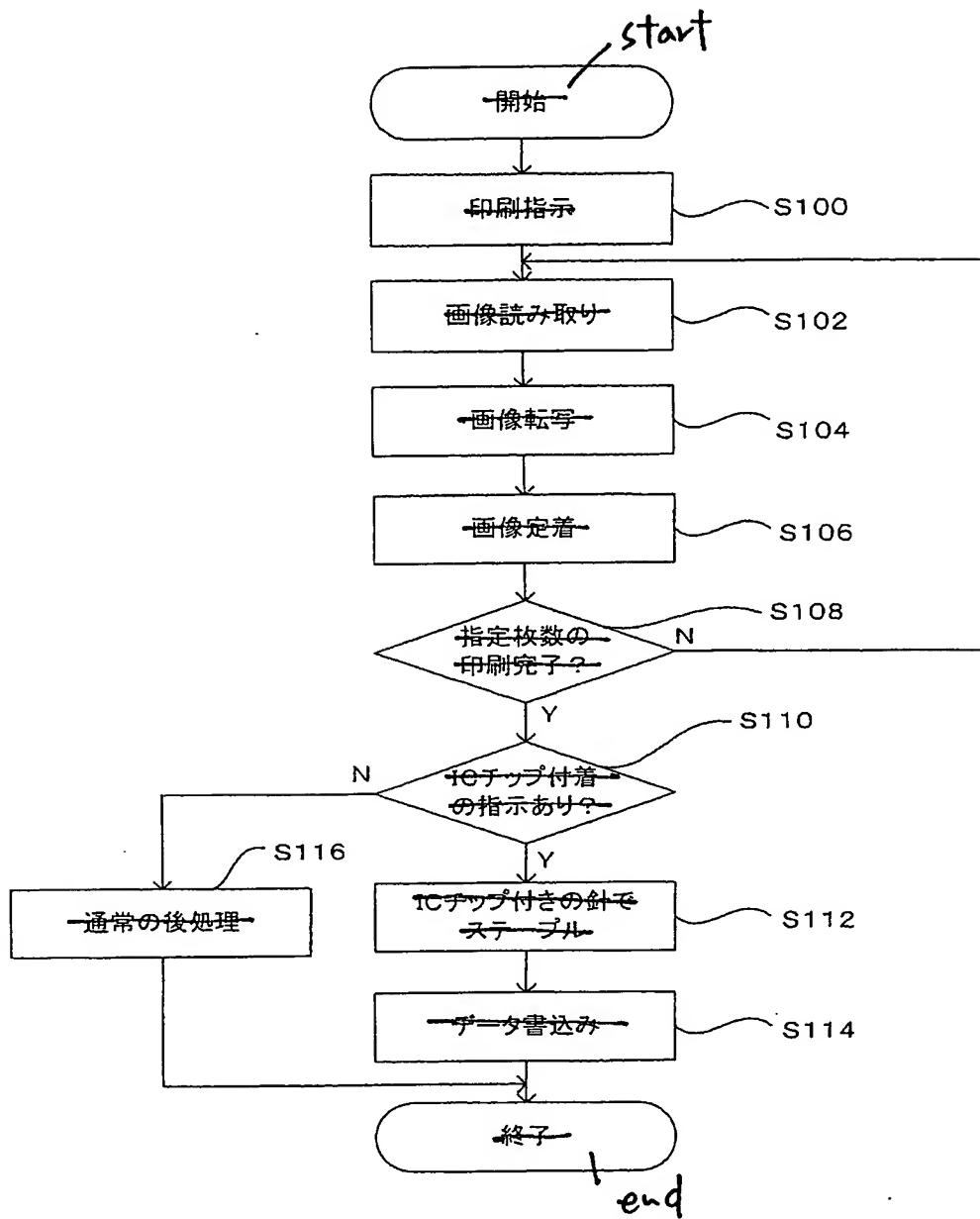


FIG. 7

700 image reading section
710 UI section
720 printing section
730 IC chip attaching section
740 data writing section

【図8】

Fig. 8



S10

FIG. 8

```
S100 instruct printing operation
S102 read image
S104 transfer image
S106 fix image
S108 designated sheet of printing operation is completed?
S110 attaching of IC chip is instructed?
S112 staple needle equipped with IC chip
S114 write data into IC chip
S116 execute normal postprocessing operation
```

Fig. 9A

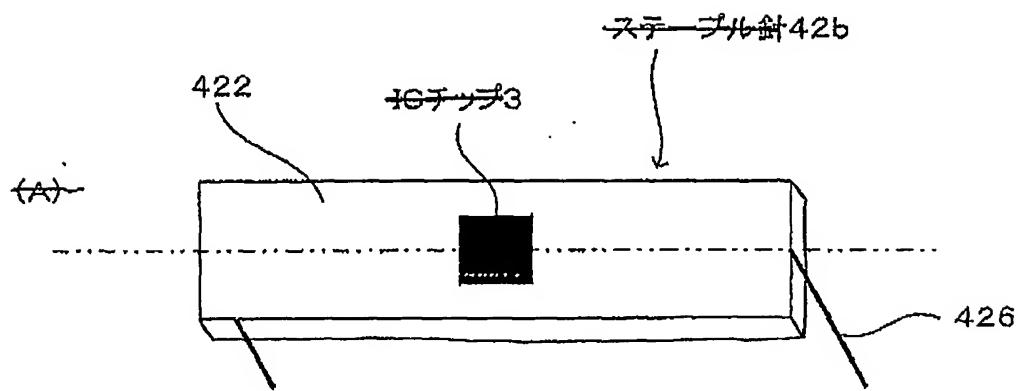


Fig. 9B

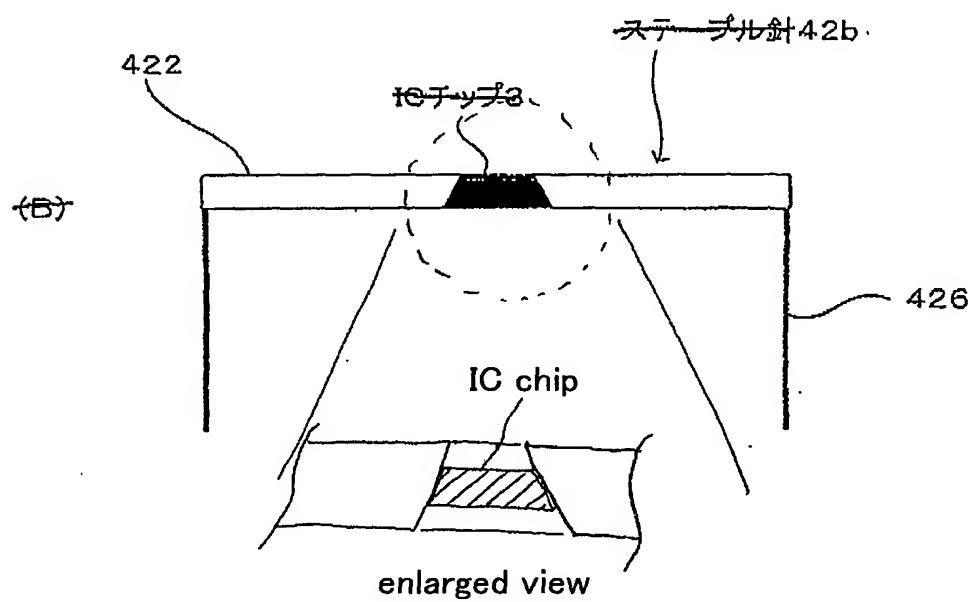


Fig. 10 A

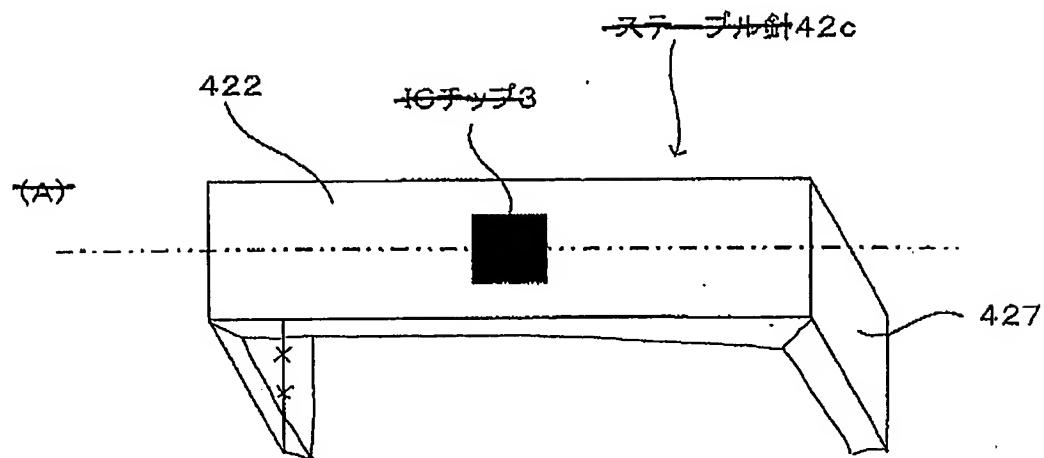


Fig. 10 B

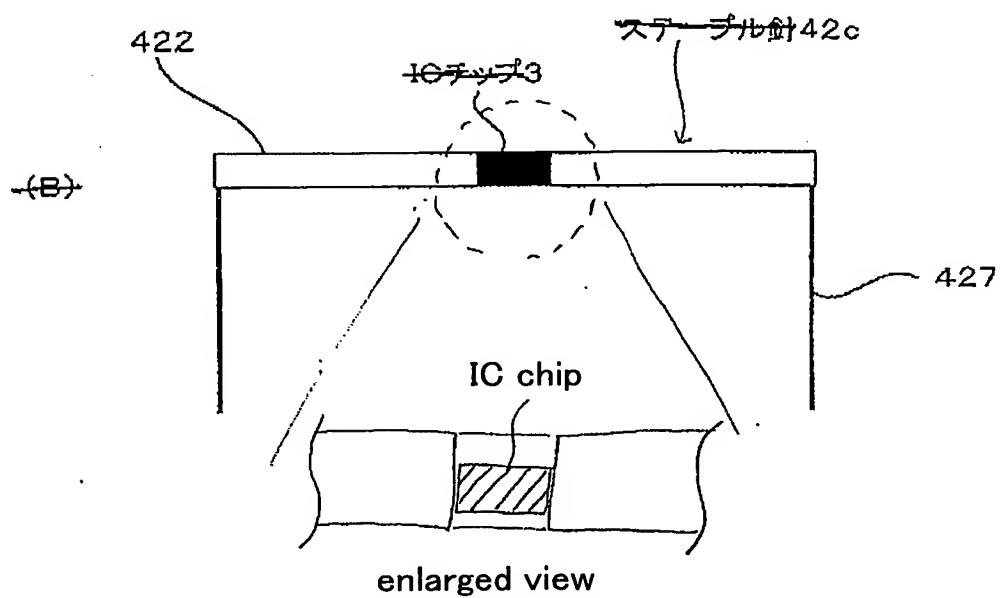


Fig. 11 A

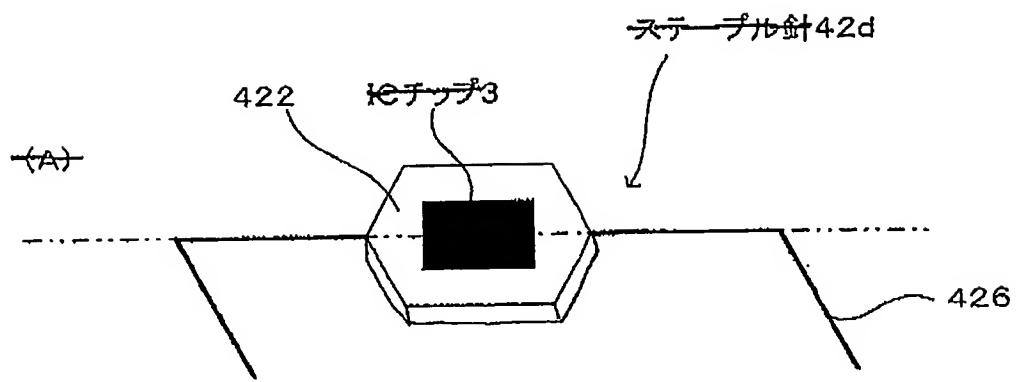
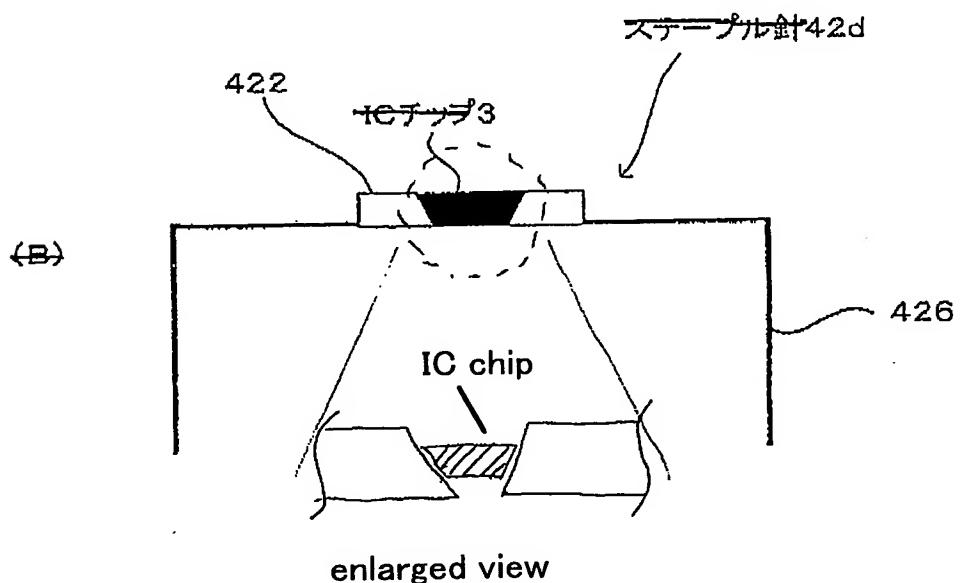


Fig. 11 B



〔図12〕

Fig. 12 A

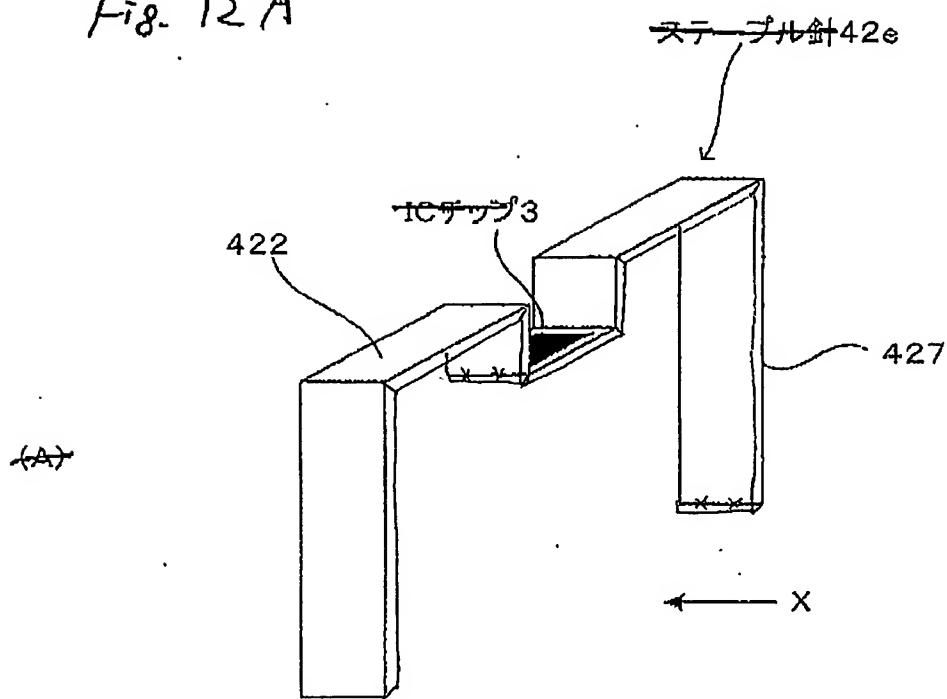


Fig. 12 B

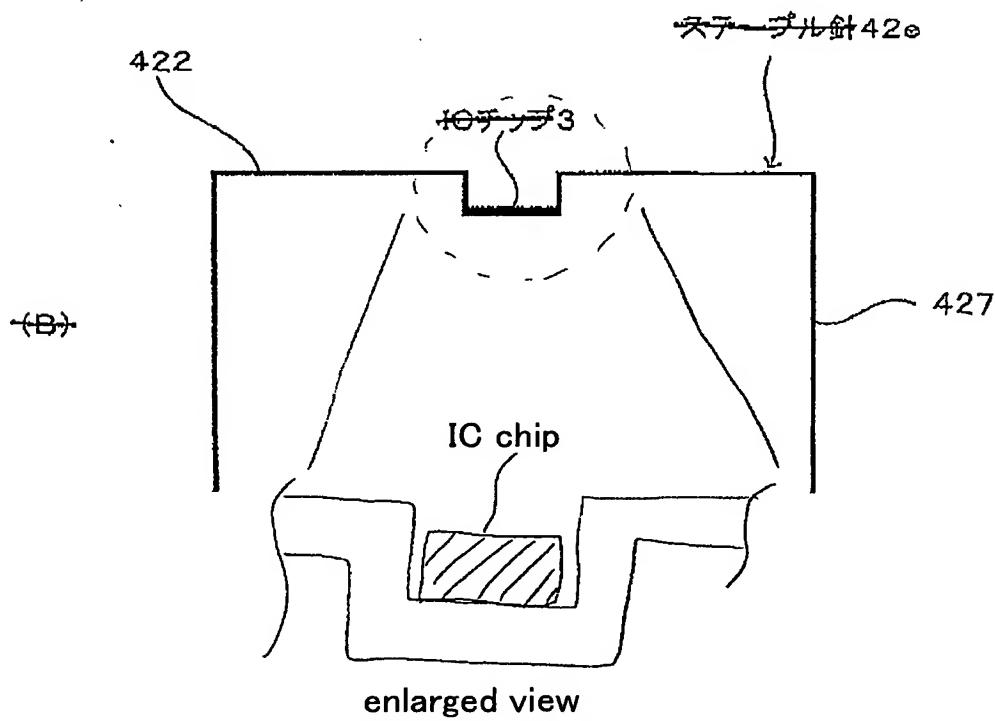
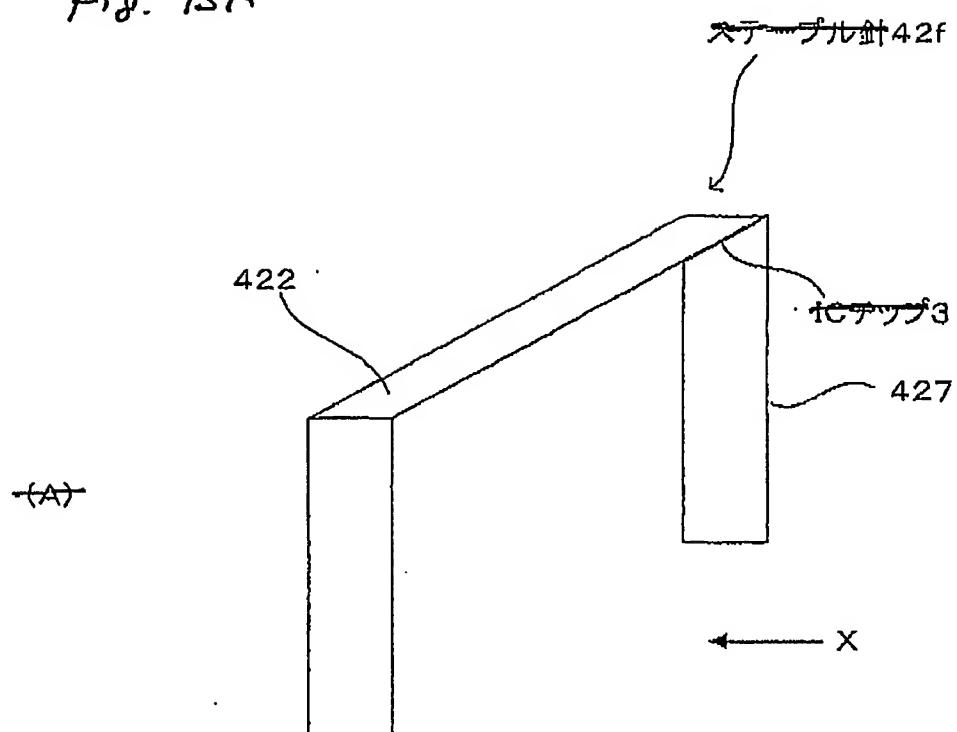


図13

Fig. 13A



Fif. 138

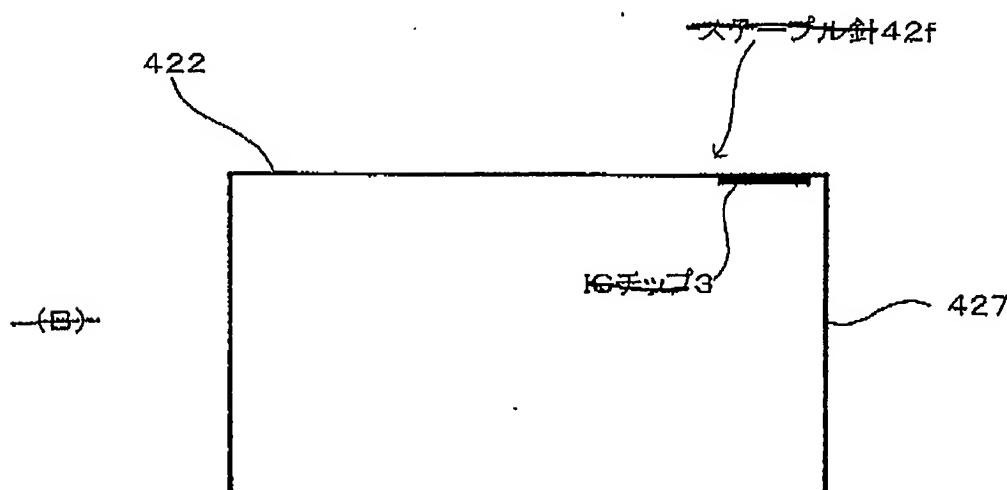
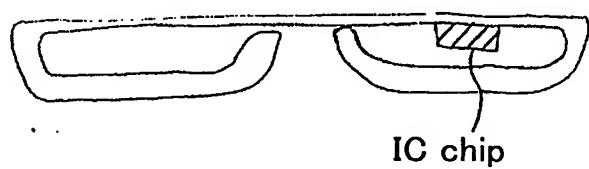


Fig. 13C



〔図14〕

Fig. 14A

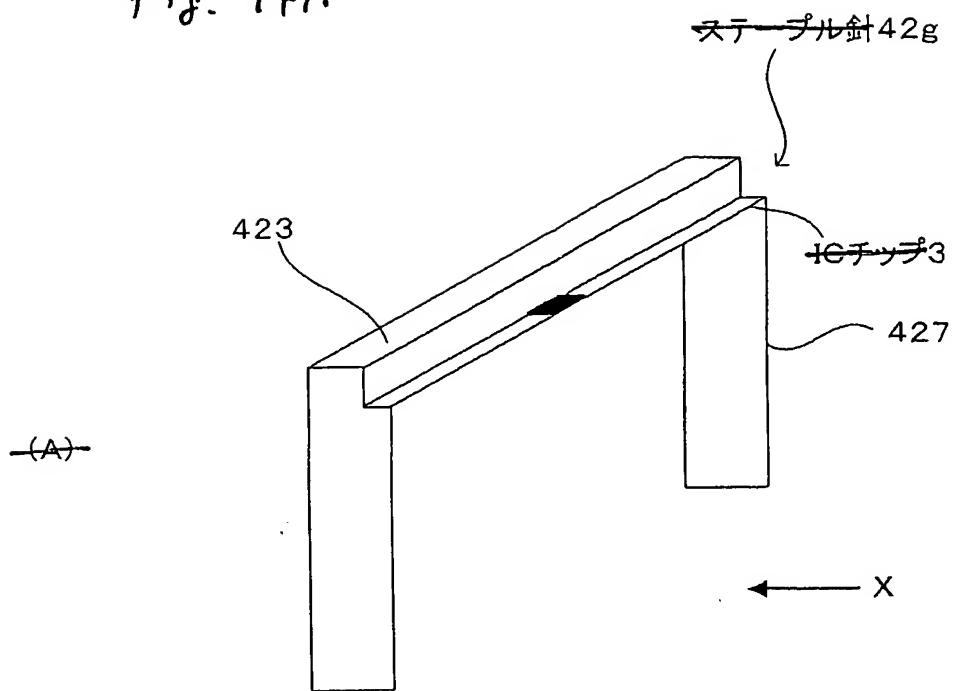


Fig. 14B

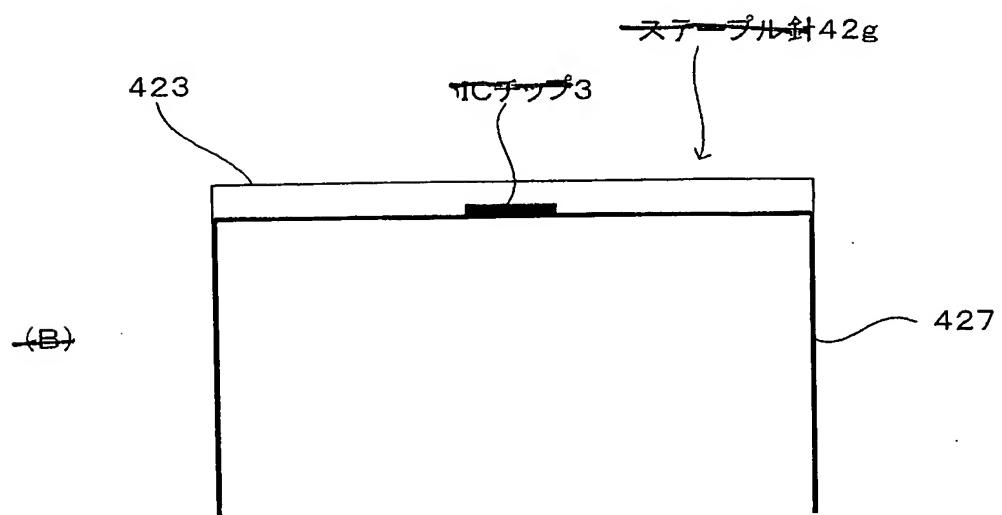
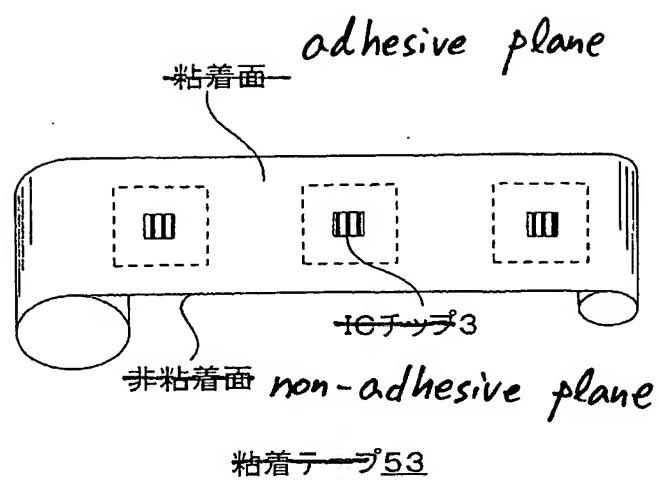


Fig. 15



粘着テープ53

Fig. 16

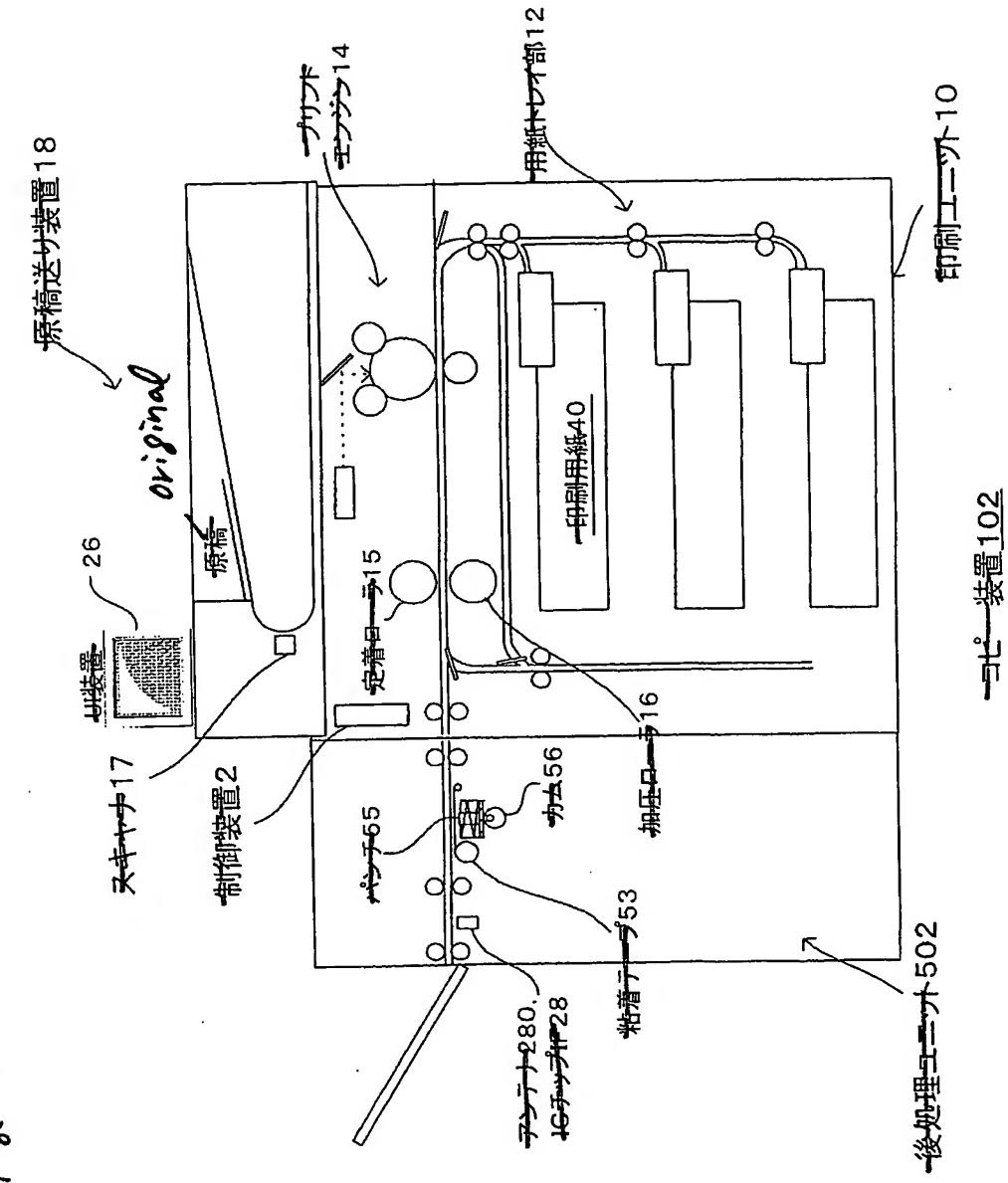


Fig. 17

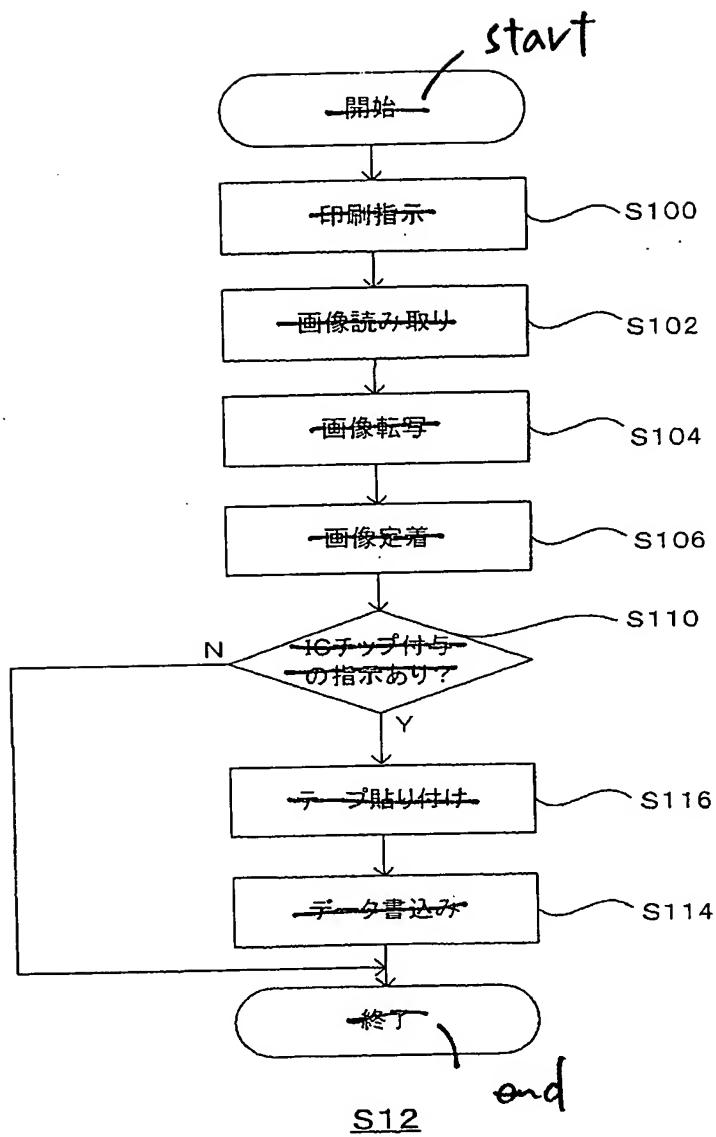


FIG. 17

```
S100 instruct printing operation
S102 read image
S104 transfer image
S106 fix image
S110 attaching of IC chip is instructed?
S114 write data into IC chip
S116 adhere tape
```

Fig.18

